

About REC Ambedkar Nagar

Rajkiya Engineering College (R.E.C.), Ambedkar Nagar was established by the Government of Uttar Pradesh in 2010 and is affiliated to Dr. A. P. J. Abdul Kalam Technical University, Lucknow. It is running B.Tech. Programs in three disciplines – Information Technology (IT), Electrical Engineering (EE), and Civil Engineering (CE). These courses are approved by AICTE, New Delhi. The students of REC Ambedkar Nagar are extensively exposed to cross-cultural environment as candidates from various other State such as Jammu & Kashmir, Madhya Pradesh, Rajasthan etc. join REC for various undergraduate programs. REC Ambedkar Nagar is fully residential institution with four hostels for boys and one for girls.

The Institute is situated 3 Km away from the city (Akbarpur Bus Stand) on Tanda road near hawaipatti (Air Strip). It is well connected through road and rail network. The nearest airport is Babatpur (Varanasi) which is about 120 Km far from the Institute.



CHIEF PATRON

Prof. Vinay Kumar Pathak

Hon'ble Vice Chancellor, Dr. A. P. J. Abdul Kalam Technical University, Lucknow

PATRON

Dr. Akhilesh Kumar Mishra

Director, Rajkiya Engineering College (R.E.C.)
Ambedkar Nagar

CONVENOR

Dr. S. P. Singh

Associate Professor & Head, EED

COORDINATOR (s)

Dr. M. Aslam Husain

Assistant Professor, EED

Dr. Puneet Joshi

Assistant Professor, EED

Dr. Yudhishtir Pandey

Assistant Professor, EED

ORGANIZING SECRETARY (s)

Mr. Vikas Patel

Assistant Professor, EED

Dr. Sanjay Agrawal

Assistant Professor, EED

Dr. Arif Iqbal

Assistant Professor, EED

TREASURER (s)

Mr. Lokesh Kumar Yadav

Assistant Professor, EED

Mr. Sonu Kumar

Assistant Professor, EED

ORGANIZING COMMITTEE

Mr. Ravindra Kumar

Mr. Abdul Hafeez

Mr. Sunil Kumar Jain

Mr. Jaswant Singh

Mr. Niteesh K Singh

Mr. Kundan Kumar

Mr. Sanjay Maurya

Mr. Vivek Tiwari

Mr. Suresh Maurya

Mr. Krishna Kumar

Two Days International Seminar

on
Recent Advances in Science & Technology (ISRAST-2020)

Feb 16-17, 2020



Organized by
Department of Electrical Engineering
Rajkiya Engineering College Ambedkar Nagar

Sponsored by

TEQIP-3
Technical Education Quality Improvement Programme

TEQIP-III

A Unit of



MHRD, Govt. of India for Implementation of World
Bank Assisted Projects in Technical Education

About the Department

The department of Electrical Engineering at Rajkiya Engineering College Ambedkar Nagar offers a vibrant environment for undergraduate education in Electrical Engineering (Established in 2010). The Department of Electrical Engineering is actively engaged in teaching and research with modern laboratories and excellent members of faculty.

The undergraduate programme provides the students with a strong background in the broad areas of Electrical Engineering namely, Power Electronics, Machines, control technology, electronics, and power & energy. The department has a very sound and young faculty strength, most of them have their master's degree and Ph.D. degree from IITs, NITs, and Central Universities.



Seminar Outline

On the first day of this International Seminar resource persons from USA will deliver expert talk on State-of-the-art Technology used around the world. Then there will be a panel to discuss education systems in countries like Greece, China, Iran and India; and how education systems in these countries contrast and compare with US education systems. In panel discussion topic regarding "how industry and academia work together in US to develop new technology by cutting edge innovations" will be discussed. On Second Day, Professors from Indian Universities and Institutions will deliver their expert talk on cutting edge technologies in Science & Technology.

Commitments expected from the participants

Participant understand that this International Seminar is mainly focused on State-of-the-art Technology used around the world. Participant will attend it completely. Attendance will be taken in each session.

TARGET PARTICIPANTS

The programme is recommended to technical teachers and research scholars who need the knowledge of such tools for their all-round academic development.

RESOURCE PERSONS

Resource Persons for this International Seminar will be Abroad as well as from Premier Institutions like IITs, NITs and State Government Engineering colleges.

1. Prof. Victor Veliadis (Executive Director and CTO of Power America, USA)
2. Prof. Jin Wang (Ohio State University, USA)
3. Prof. Babak Parkhideh (University of North Carolina, USA)
4. Dr. Brij N. Singh (Senior Staff Engineer in John Deere Inc., USA)
5. Luisa Maria Patino Parra (Spain)
6. Marta Quintana Laporta (Spain)
7. Prof. S. P. Tewari, IIT(BHU), Varanasi
8. Dr. S. R. Mohanty, IIT(BHU), Varanasi
9. Dr. Dipayan Guha, MNNIT, Allahabad
10. Dr. Vijay Pratap Singh, REC, Sonbhadra

REGISTRATION

There is no Registration fee for the participants.

Boarding/lodging:

The participants will have to make stay arrangements at their own during Seminar. In-campus Hostel Accommodation may be provided free of cost but it will depend on the availability. Participants will have to pay for accommodation provided outside the campus.

TA/DA will not be provided to participants.

Contact details:

Email: mahusain87@gmail.com, vikaspatel@recabn.ac.in

Mob.: 9451663915, 8808517767

Registration Form

Two Days International Seminar

on

Recent Advances in Science & Technology (ISRAST-2020)

Feb 16-17, 2020

Organized by

Department of Electrical Engineering
Rajkiya Engineering College Ambedkar Nagar

Sponsored by

TEQIP-III

Full Name: _____

Designation, Department and Organization: _____

Email Address: _____

Correspondence Address: _____

Mobile number: _____

Accommodation[#] Required (Yes /No): _____

Whether a TEQIP network Institute: Yes / No

Signature of the Participant

Recommended/Not Recommended

Signature and Seal

Head of the Department/Institution



Dr. Victor Veliadis is Executive Director and CTO of Power America, which is a U.S. Department of Energy wide bandgap power electronics Manufacturing Innovation Institute. Dr. Veliadis manages a budget in excess of \$30 million per year that he strategically allocates to over 35 industrial, University, and National-Laboratory projects, to enable U.S. leadership in WBG power electronics manufacturing, work force development, job creation, and energy savings. Dr. Veliadis has given over 60 invited presentations/tutorials, and keynotes at major conferences in India, Korea, China, Europe and the U.S. He is an IEEE Fellow and an IEEE EDS Distinguished Lecturer. Dr. Veliadis has 25 issued U.S. patents, 6 book chapters, and over 120 peer-reviewed technical publications to his credit. He is also Professor in Electrical and Computer Engineering at North Carolina State University. Dr. Veliadis received the Ph.D. degree in Electrical and Computer Engineering from Johns Hopkins University in 1995. Prior to taking an executive position at Power America in 2016, Dr. Veliadis spent 21 years in the semiconductor industry where his work included design, fabrication, and testing of 1-12 kV SiC SiTs, JFETs, MOSFETs, Thyristors, and JBS and PiN diodes, as well as financial and operations management of a commercial foundry.



Dr. Jin Wang received his Ph.D. degree from Michigan State University, East Lansing, in 2005. From Sept., 2005 to Aug. 2007, he worked at the Ford Motor Company as a Core Power Electronics Engineer. He joined the Ohio State University in 2007 as an Assistant Professor and was promoted to Associate professor in 2013 and full professor in 2017. His research interests include wide bandgap power devices and their applications, high-voltage and high-power converter/inverters, integration of renewable energy sources, and electrification of transportation. Dr. Wang has over 180 peer-reviewed journal and conference publications and 8 patents. Dr. Wang received the IEEE Power Electronics Society Richard M. Bass Young Engineer Award and the National Science Foundation's CAREER Award in 2011. At The Ohio State University, Dr. Wang received the Ralph L. Boyer Award for Excellence in Undergraduate Teaching Innovation in 2012, the Lumley Research Award in 2013 and the Harrison Faculty Award for Excellence in Engineering Education in 2017. Dr. Wang served as the General Chair and the Steering Committee Chair for the IEEE Future Energy Challenge in 2016 and 2017, respectively. Dr. Wang initiated and served as the General Chair for the 1st IEEE Workshop on Wide Bandgap

Power Devices and Applications in 2013. Dr. Wang had been an Associate Editor for the IEEE Transactions on Industry Applications from 2008 to 2014. Currently, Dr. Wang serves an Associate Editor for the IEEE Transactions on Power Electronics and the IEEE Journal of Emerging and Selected Topics in Power Electronics (J-ESTPE).



Dr. Babak Parkhideh is an Associate Professor at the University of North Carolina-Charlotte where he is the founding director of the Photovoltaic Integration Laboratory since 2012. He is also developing the Power Electronics hands-on training program at the US Naval Sea Systems Command (NAVSEA), Washington, DC. Initiated from his research laboratory and support from the National Science Foundation, he commercializes ultrafast sensing solutions for post-silicon power electronics at Telli Technologies Inc. where he serves as the founder and president of the company. He completed his PhD at North Carolina State University, Raleigh, NC, Master's at RWTH-Aachen University, Germany, and bachelor's (with honor and distinction) at Tehran University all in Electrical Engineering. Dr. Parkhideh has published over 90 papers in journals and conference proceedings and holds four issued and nine pending patents in the broad area of power electronics. Dr. Parkhideh is Senior Member of IEEE.



Brij N. Singh is a Senior Staff Engineer in John Deere Inc., USA and leading the US Department of Energy – Power America (DOE-Power America) funded project to develop a 200 kW SiC inverter for heavy-duty vehicle applications. Brij has earned BE degree in Electrical Engineering from MMMUT Gorakhpur in 1989, ME degree in Electrical Engineering from IIT Roorkee in 1991, and Ph.D. degree in Electrical Engineering from the IIT Delhi in 1996. In 1996, Brij joined the École de Technologie Supérieure, Université du Québec, Montreal, QC, Canada, as a Post-Doctoral Fellow. In 1999, Brij joined Concordia University, Montreal, QC, Canada as a Research Fellow. In 2000, Brij joined the Department of Electrical Engineering and Computer Science, Tulane University, New Orleans, Louisiana, as an Assistant Professor. In 2007, Brij joined John Deere in Fargo, North Dakota as a Power Electronics Staff Engineer, where he is currently a Senior Staff Engineer - Power Electronics and Advanced Technology. Brij is also an Adjunct Professor in the Dept. of Electrical and Computer Engineering in North Dakota State University, Fargo, USA. In Tulane, Brij received numerous teaching awards for outstanding instructions in electrical engineering. In John Deere, Brij received numerous awards for product and technology innovations and team collaboration activities. Brij has published over 90 research papers in various Journals including IEEE Transactions and IET Journals. Brij has 26 awarded US patents, one trade secret, and numerous pending patents. Brij's scholarly work and publications have been cited by his peers over 5000 times. Brij is a senior member of the IEEE.